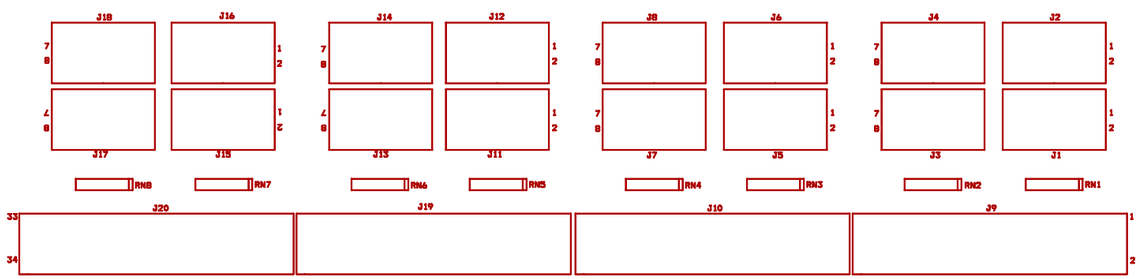
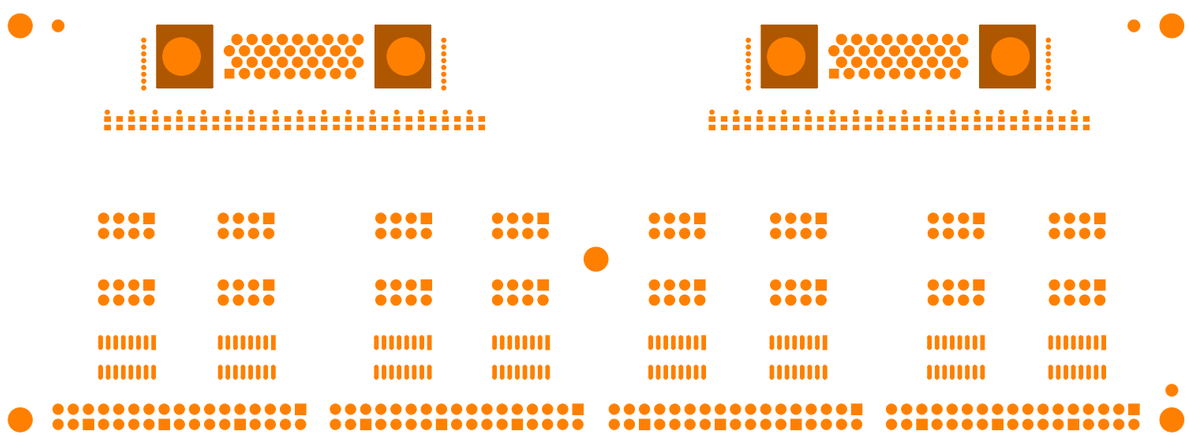
SS LEGEND TOP SIDE

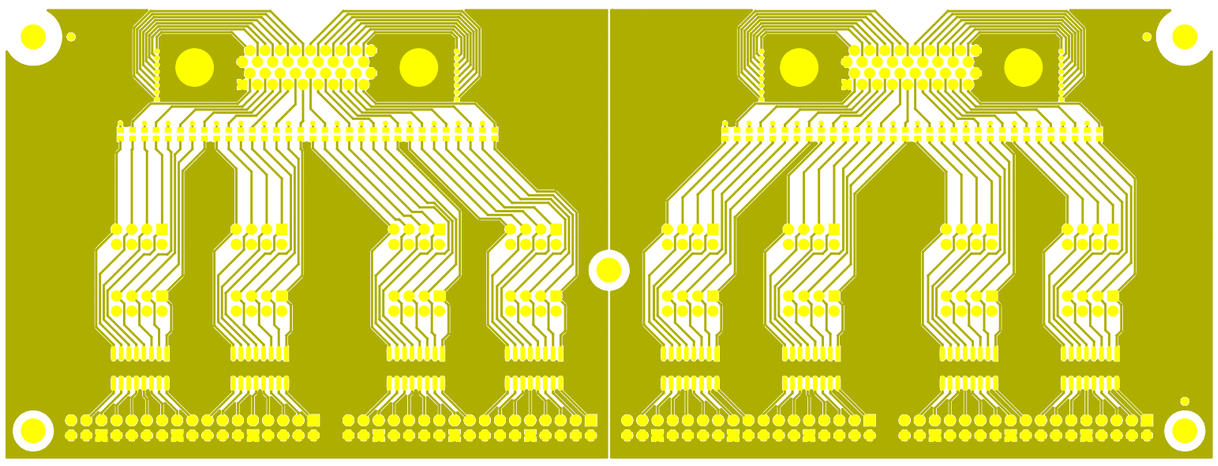
D-ZERO
L1 CALTRIG Patch Panel
3823-122-EB-330505
J. MOUA

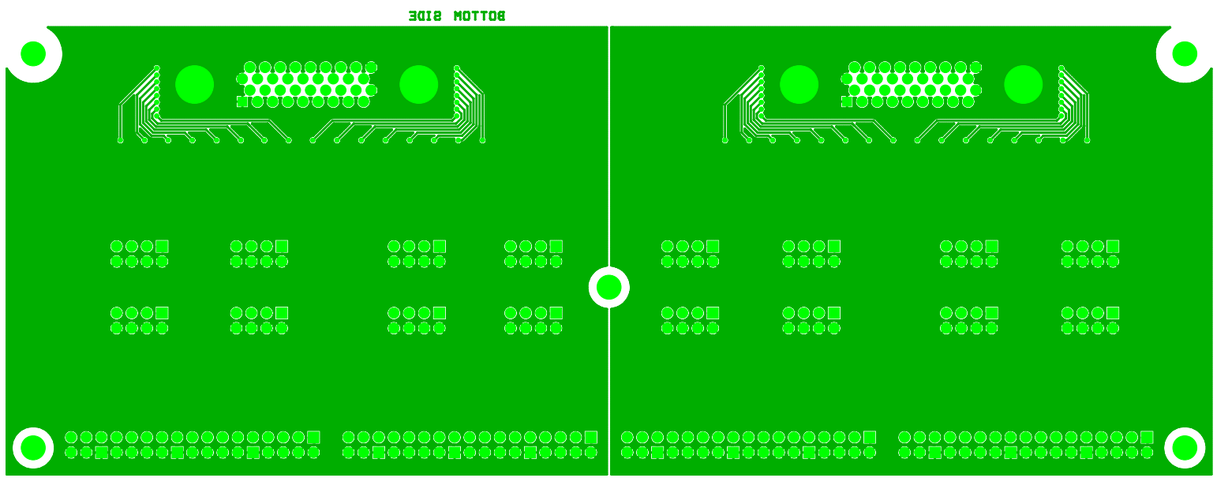


SOLDER MASK TOP SIDE

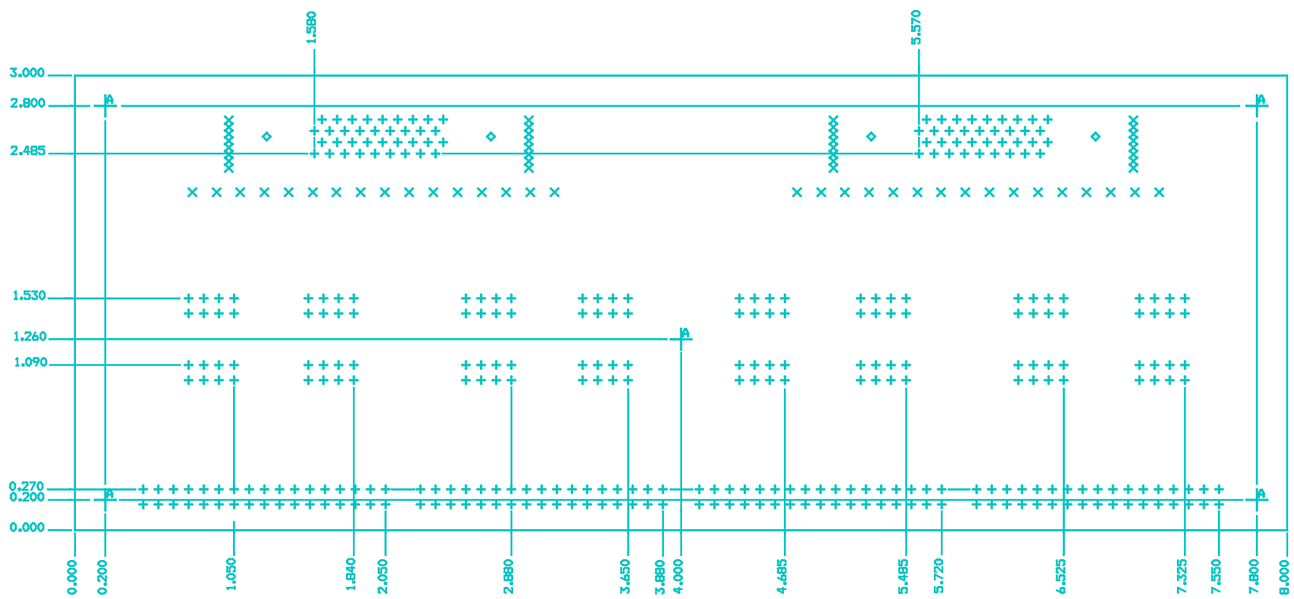


TOP SIDE




BOTTOM SIDE

DRILL CHART				
SYM	DIAM	TOL	QTY	NOTE
×	0.020		64	
+	0.035		336	
◇	0.125		4	
⊥A	0.140		5	NON-PLATED
TOTAL			409	



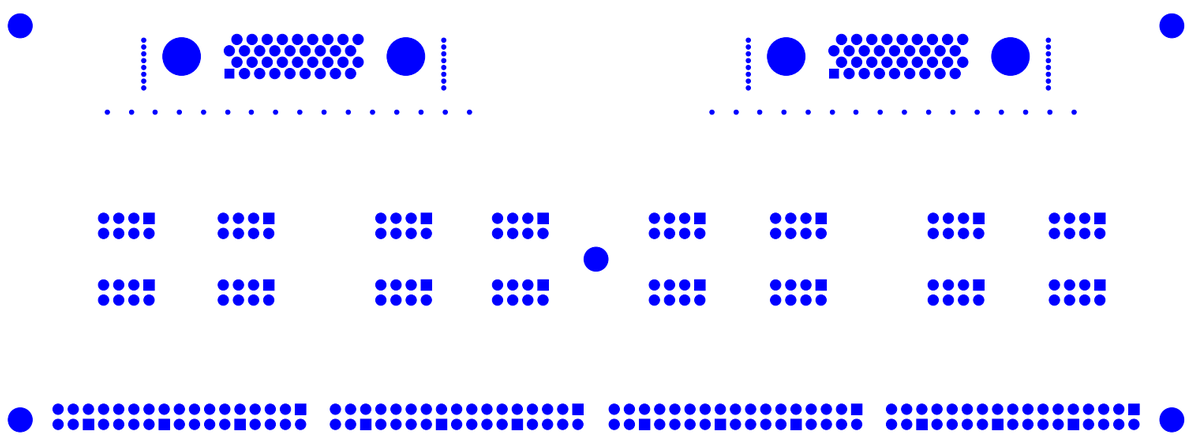
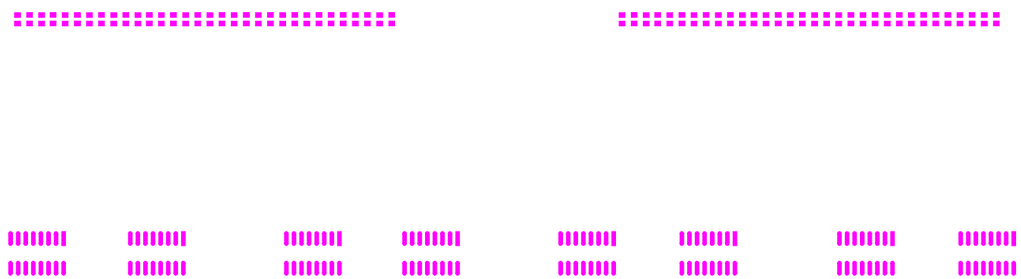
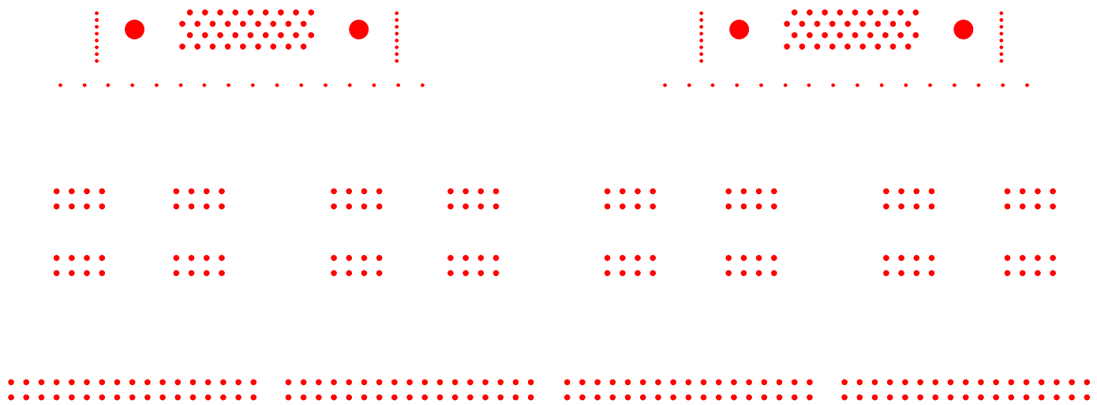
BOARD NAME L1 CALTRIB Patch Panel

BOARD NUMBER 3823-122-EB-330505

NOTES:

1. MATERIAL:
   - ☒ NEMA FR4 EPOXY GLASS LAMINATE
   - ☐ PTFE
   - ☐ _____
   - ☐ SINGLE SIDED
   - ☒ DOUBLE SIDED
   - ☐ MULTI LAYER ___X___ LAYERS
   - ☒ .062 THK. ☐ .093 THK. ☐ OTHER_____
   - ☐ .5 OZ. CU. ☒ 1 OZ. CU. ☐ 2 OZ. CU. ☐ 3 OZ. CU.
2. DRILLING:
   SEE DRILL CHART, ALL HOLE DIAMETERS SPECIFIED ARE FINISHED HOLE DIAMETERS.
3. PLATING:
   - ☐ PLATED THRU HOLES: COPPER .0007 MIN. THK. CIRCUITRY 60/40 TIN-LEAD (REFLOW) .0007 MIN. THK.HOLES: .001 MIN THK. WALL.
   - ☐ NON-PLATED THRU HOLES CIRCUITRY 60/40 ELECTROPLATED TIN-LEAD (REFLOW) .0007 MIN.THK.
   - ☒ SELECTIVE SOLDER PLATE: PLATED THRU HOLES COPPER .0007 MIN. THK. PAD AREAS 60/40 TIN-LEAD .0001-.0003 MIN.
   - ☐ CONNECTOR FINGERS: NICKEL .0003 MIN. THK. GOLD 50 MICRO-INCHES MIN. THK.
4. CONDUCTOR WIDTH AND SPACING
   MINIMUM TRACE WIDTH ____.010_____
   MINIMUM CLEARANCE ___.010_____
5. NUMBER OF SM PADS
   TOP___128___
   BOTTOM ____0_____
6. CONTROLLED IMPEDANCE
   - ☐ YES IMPEDANCE____XXX OHMS_____
   - ☒ NO
7. SOLDER MASK:
   - ☒ TOP SIDE
   - ☒ BOTTOM SIDE
   - ☐ PD401 ☐ _____
   - ☒ GREEN ☐ _____
8. COMPONENT LEGEND:
   - ☒ TOP SIDE
   - ☐ BOTTOM SIDE
   - ☒ WHITE EPOXY INK
9. FINAL FABRICATION:
   FABRICATE TO DIMENSIONS SHOWN.
10. DO NOT SCALE DRAWING.
11. REMOVE ALL BURRS AND SHARP EDGES.
12. MATERIAL SUPPLIED
    EXCELLON NC DRILL FILES
    GERBER PHOTOPLOT FILES

SOLDER MASK BOTTOM SIDE



SOLDER PASTE TOP SIDE